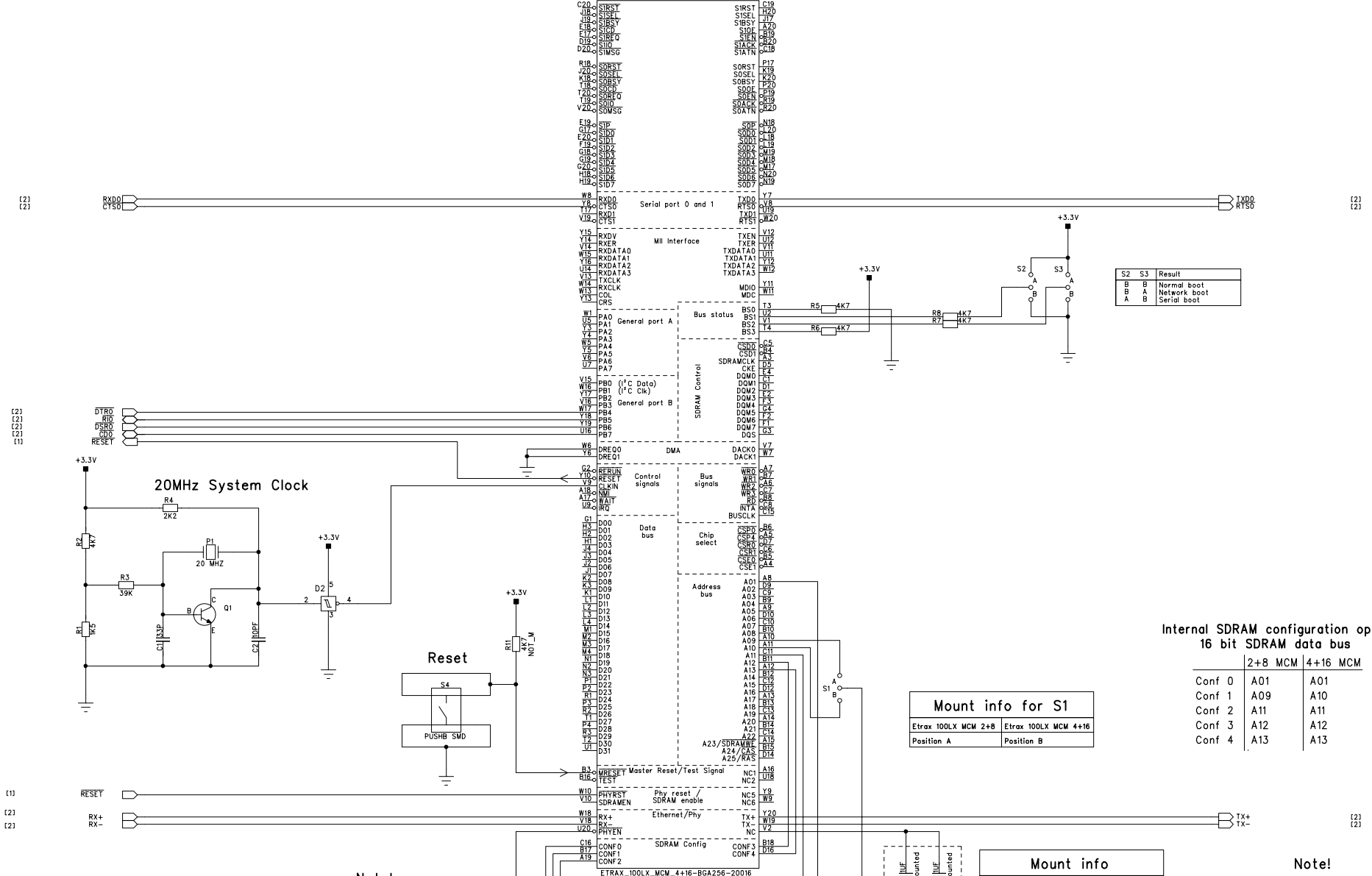


1.MCM

6 5 4 3 2 1



S2	S3	Result
B	B	Normal boot
B	A	Network boot
A	B	Serial boot

Internal SDRAM configuration option
16 bit SDRAM data bus

	2+8 MCM	4+16 MCM
Conf 0	A01	A01
Conf 1	A09	A10
Conf 2	A11	A11
Conf 3	A12	A12
Conf 4	A13	A13

Mount info for S1

Position A	Position B
Etrax 100LX MCM 2+8	Etrax 100LX MCM 4+16

Mount info

Mount C3 and C4	Not mount C3 and C4
Etrax 100LX MCM 2+8	Etrax 100LX MCM 4+16

Note!
For use of MCM with external SDRAM and 32 bit data bus, see technical doc. at:
www.developer.axis.com

Note!
PHYRST and SDRAMEN are NC pins on Etrax 100LX MCM 2+8.
When 4+16 MCM is used, PHYRST must be connected to the RESET signal. It can be connected direct or via logic that make it possible to reset the internal PHY without resetting the MCM.
SDRAMEN is only used if the internal SDRAM shall be disabled. This signal have internal pull-up.

Name: MCM 4+16 SERIAL SERVER	Part No: XXXXX	Rev: R3
CIRCUIT DIAGRAM AXIS COMMUNICATIONS AB, SWEDEN	Date: Jun 14, 2004	Des: NA
	Sheet 1 of 5	Appr:

2.RS232/NETWORK

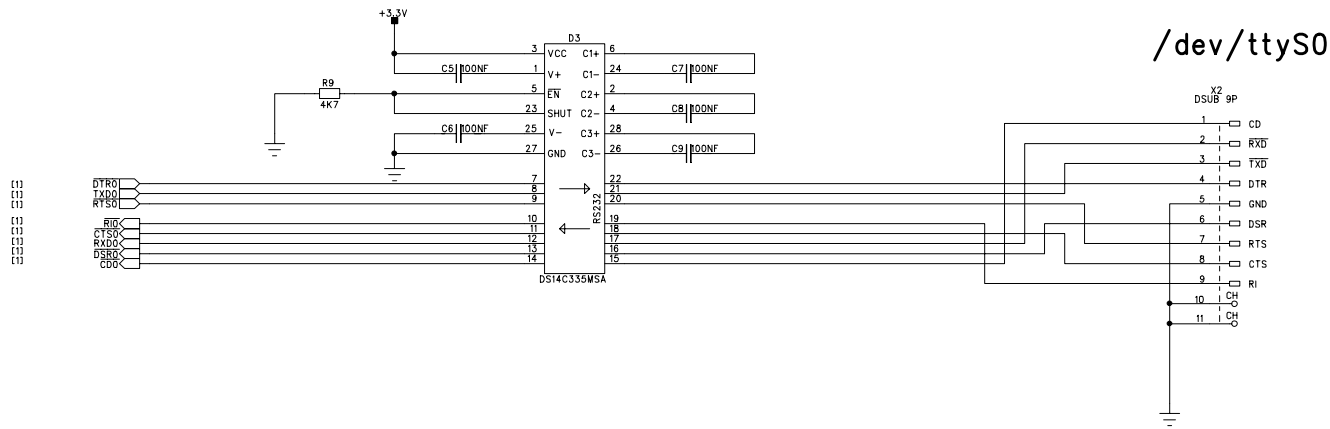
6 5 4 3 2 1

D

C

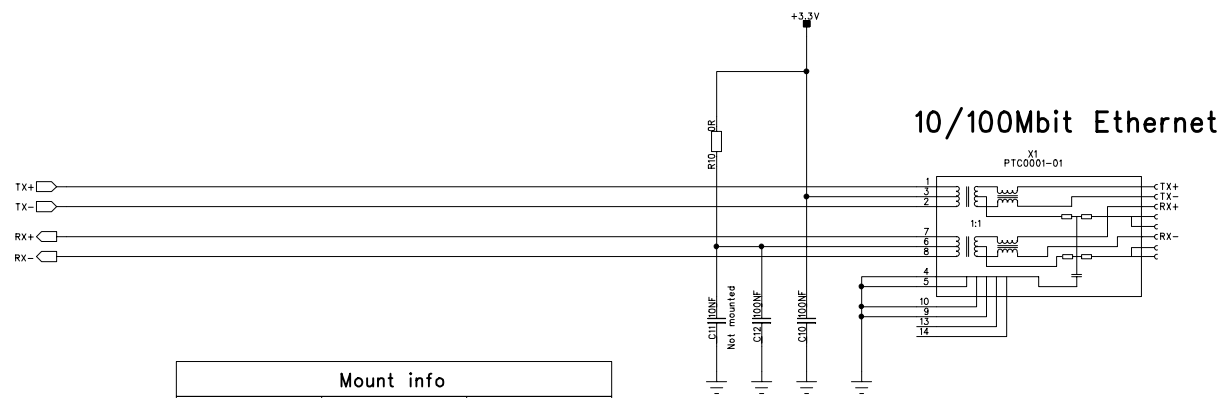
B

A



(1)
(1)
(1)
(1)
(1)
(1)
(1)
(1)

(1)
(1)
(1)
(1)



Mount info		
Etrax 100LX MCM 2+8 E1 Axis Part no 18886	Etrax 100LX MCM 4+16 E1 Axis Part no 20016	Etrax 100LX MCM 4+16 E2 Axis Part no 22545
Mount C11, R10 Not mount C10, C12	Mount C10, C12 Not mount C11, R10	Mount C10, C12, R10 Not mount C11

Name:	MCM 4+16 SERIAL SERVER	Part No:	XXXXX	Rev:	R3
	CIRCUIT DIAGRAM	Date:	Jun 14, 2004	Des:	NA
	AXIS COMMUNICATIONS AB, SWEDEN	Sheet	2 of 5	Appr:	

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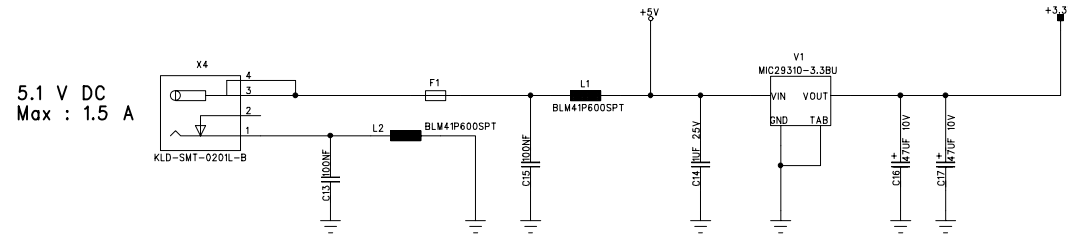
4

3

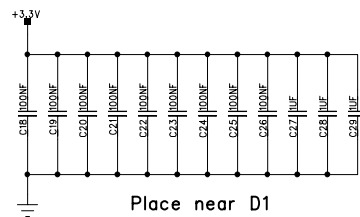
2

1

3.Power/Decoupling



MCM +3.3V power decoupling capacitors



Name:	MCM 4+16 SERIAL SERVER	Part No:	XXXXX	Rev:	R3
	CIRCUIT DIAGRAM	Date:	Jun 14, 2004	Des:	NA
	AXIS COMMUNICATIONS AB, SWEDEN	Sheet	3 of 5	Appr:	

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5

4

3

2

1

4.Design Rules**Design Rules****Page 1**

RX+- / TX+- should be routed adjacent to a ground plane.
Route RX+- / TX+- with matched length and characteristic differential impedance of 100 ohm.

Page 2

RX+- / TX+- should be routed adjacent to a ground plane.
Route RX+- / TX+- with matched length and characteristic differential impedance of 100 ohm.

Page 3

Place decoupling capacitors close to D1.

Name:	MCM 4+16 SERIAL SERVER	Part No:	XXXXX	Rev:	R3
	CIRCUIT DIAGRAM	Date:	Jun 14, 2004	Des:	NA
	AXIS COMMUNICATIONS AB, SWEDEN	Sheet	4 of 5	Appr:	

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1

5.Revision

REVISION HISTORY			
DATE	REV	BY	DESCRIPTION
April 24, 2003	R0	NA	Initial version
May 21, 2003	R1	NA	Added R11 and corrected the SDRAM conf. table at page 1
Nov 17, 2003	R2	NA	Added C12 and corrected the center tap termination for X1 at page 2
Jun 14, 2004	R3	NA	Added mount info for MCM 4+16 E2 at page 2.

Note!

This design has not been produced and verified.
and can contain errors.

Name:	MCM 4+16 SERIAL SERVER	Part No:	XXXXX	Rev:	R3
	CIRCUIT DIAGRAM	Date:	Jun 14, 2004	Des:	NA
	AXIS COMMUNICATIONS AB, SWEDEN	Sheet	5 of 5	Appr:	